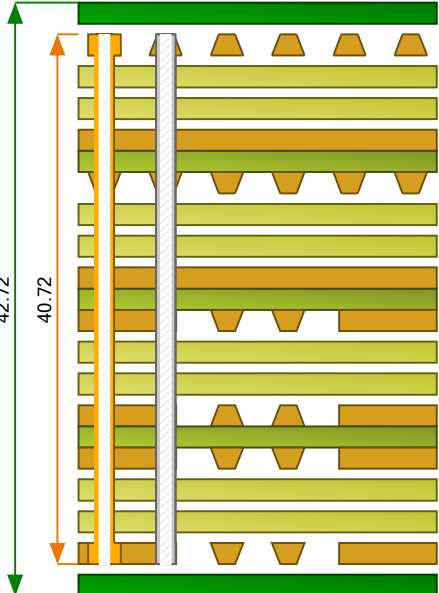



Layer	Stack up	Description	Processed Thickness	Isolation Distance (Summed)	Copper Coverage	Impedance ID	εr	Loss Tangent		
1		Taiyo PSR 4000 HFX DI-GREEN	1.000				3.500	0.0270		
		Copper Foil 12 microns	1.850		100.000	1, 2, 3, 4, 5, 6, 7				
2		Iteq IT180A Prepreg 106 RC71-NEW	1.775	4.362			3.790	0.0150		
		Iteq IT180A Prepreg 1080 RC65-NEW	2.588	-			3.860	0.0150		
3			1.260		50.000					
		Iteq IT180A 3 mil core 1/H	3.000	3.000			3.930	0.0150		
4			0.689		30.000	8, 9, 10, 11, 12, 13				
		Iteq IT180A Prepreg 1080 RC65-NEW	2.468	4.936			3.860	0.0150		
5		Iteq IT180A Prepreg 1080 RC65-NEW	2.468	-			3.860	0.0150		
			1.260		60.000					
6		Iteq IT180A 6 mil core 1/1	6.000	6.000			4.460	0.0150		
			1.260		60.000	14, 15				
7		Iteq IT180A Prepreg 1080 RC65-NEW	2.468	4.936			3.860	0.0150		
		Iteq IT180A Prepreg 1080 RC65-NEW	2.468	-			3.860	0.0150		
8			0.689		30.000					
		Iteq IT180A 3 mil core H/1	3.000	3.000			3.930	0.0150		
9			1.260		50.000	16, 17				
		Iteq IT180A Prepreg 1080 RC65-NEW	2.588	4.362			3.860	0.0150		
10		Iteq IT180A Prepreg 106 RC71-NEW	1.775	-			3.790	0.0150		
		Copper Foil 12 microns	1.850		100.000	18, 19, 20, 21, 22, 23, 24				
11		Taiyo PSR 4000 HFX DI-GREEN	1.000				3.500	0.0270		


Copper Thickness = 10.118 | Dielectric Thickness = 30.598 | Solder Mask Thickness = 2.000 | Stack Up Thickness = 40.715 | Stack Up Thickness with Soldermask = 42.715

Impedance ID	Impedance Signal Layer	Structure Name	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Ground Strip Separation (D1)	Calculated Impedance	Target Impedance	Tol (+/- %)
1	1	Coated Microstrip 2B	2	0	15.400	14.400	0.000	0.000	33.030	33.000	10.000
2	1	Coated Microstrip 2B	2	0	11.300	10.300	0.000	0.000	39.970	40.000	10.000
3	1	Coated Microstrip 2B	2	0	7.500	6.500	0.000	0.000	49.880	50.000	10.000
4	1	Edge Coupled Coated Microstrip 2B	2	0	10.300	9.300	4.000	0.000	66.100	66.000	10.000
5	1	Edge Coupled Coated Microstrip 2B	2	0	8.000	7.000	5.700	0.000	80.100	80.000	10.000
6	1	Edge Coupled Coated Microstrip 2B	2	0	4.700	3.700	4.000	0.000	90.460	90.000	10.000

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Date: 09-01-2024	Associated Documents:						
Author: -							
Department: Engg-CAM							
Site: www.hiqelectronics.com							

Impedance ID	Impedance Signal Layer	Structure Name	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Ground Strip Separation (D1)	Calculated Impedance	Target Impedance	Tol (+/- %)	
7	1	Edge Coupled Coated Microstrip 2B	2	0	4.000	3.000	4.700	0.000	99.790	100.000	10.000	
8	3	Offset Stripline 1B1A	2	4	4.800	4.400	0.000	0.000	40.150	40.000	10.000	
9	3	Offset Stripline 1B1A	2	4	3.000	2.600	0.000	0.000	50.670	50.000	10.000	
10	3	Edge Coupled Offset Stripline 1B1A	2	4	4.000	3.600	4.000	0.000	80.460	80.000	10.000	
11	3	Edge Coupled Offset Stripline 1B1A	2	4	3.500	3.100	5.800	0.000	89.960	90.000	10.000	
12	3	Edge Coupled Offset Stripline 1B1A	2	4	3.000	2.600	9.300	0.000	100.010	100.000	10.000	
13	3	Edge Coupled Offset Stripline 1B1A	2	4	3.000	2.600	6.500	0.000	97.490	133.000	10.000	
14	5	Offset Stripline 1B1A	4	6	6.700	5.700	0.000	0.000	39.890	40.000	10.000	
15	5	Edge Coupled Offset Stripline 1B1A	4	6	5.000	4.000	4.600	0.000	79.890	80.000	10.000	
16	7	Offset Stripline 1B2A	6	8	6.000	5.000	0.000	0.000	33.420	33.000	10.000	
17	7	Edge Coupled Offset Stripline 1B2A	6	8	5.250	4.250	4.000	0.000	66.110	66.000	10.000	
18	8	Coated Microstrip 2B	7	0	15.400	14.400	0.000	0.000	33.030	33.000	10.000	
19	8	Coated Microstrip 2B	7	0	11.300	10.300	0.000	0.000	39.970	40.000	10.000	
20	8	Coated Microstrip 2B	7	0	7.500	6.500	0.000	0.000	49.880	50.000	10.000	
21	8	Edge Coupled Coated Microstrip 2B	7	0	10.300	9.300	4.000	0.000	66.100	66.000	10.000	
22	8	Edge Coupled Coated Microstrip 2B	7	0	8.000	7.000	5.700	0.000	80.100	80.000	10.000	
23	8	Edge Coupled Coated Microstrip 2B	7	0	4.700	3.700	4.000	0.000	90.460	90.000	10.000	
24	8	Edge Coupled Coated Microstrip 2B	7	0	4.000	3.000	4.700	0.000	99.790	100.000	10.000	

Notes

StackName:	Version:	Revision:	Modification:	Date of Revision:	Editor	Pag	
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